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181 7590 06/17/2004

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/642,658	08/19/2003	Hidegori Sato	XA-9519A	3468

TITLE OF INVENTION: MANUFACTURING METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	09/17/2004
EXAMINER	ART UNIT		CLASS-SUBCLASS		
PERKINS, PAMELA E	2822		438-791000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Renesas Technology Corp.
Hitachi ULSI Systems Co., Ltd.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Tokyo, Japan
Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); individual corporation or other private group entity government

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Mitchell W. Shapiro, Reg. No. 31,568 09/16/2004

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